

# University of Mumbai



1T00934

Winter 2025

## EXAMINATION TIME TABLE

**PROGRAMME - S.E. (Electronics & Computer Science) (Choice Based)  
(R-2020-21 'C' Scheme)**

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	40721	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	40722	Electronic Circuits
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	40723	Controls & Instrumentation
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	40724	Microprocessors & Microcontrollers
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	40725	Discrete Structures & Automata Theory

Important Note: • The candidates appearing for the examination should report 20 minutes before the start of examination.  
 • Mobile phones and other electronic gadgets are prohibited in the examination hall.  
 • Change if any, in the timetable shall be communicated on the University website.

Mumbai - 400 098  
 20<sup>th</sup> October, 2025.

*yuktaundale!*  
 Dr. Pooja Raundale  
 Director  
 Board of Examinations & Evaluation

# University of Mumbai



1T00734

Winter 2025

## EXAMINATION TIME TABLE

**PROGRAMME - S.E. (Computer) (Choice Based) (R-2020-21 'C' Scheme)**

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	40521	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	40522	Analysis of Algorithm
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	40523	Database Management System
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	40524	Operating System
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	40525	Microprocessors

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*poojaraundale*!

Mumbai - 400 098  
20<sup>th</sup> October, 2025.

Dr. Pooja Raundale  
Director  
Board of Examinations & Evaluation

# University of Mumbai



**1TO1814**  
Winter 2025

## EXAMINATION TIME TABLE

**PROGRAMME - S.E. Artificial Intelligence & Data Science (R-2019-20) ('C' Scheme)**  
SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	38911	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	38912	Analysis of Algorithm
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	38913	Database Management System
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	38914	Operating System
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	38915	Microprocessor

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Mumbai - 400 098  
20<sup>th</sup> October, 2025.

*Raundale*  
Dr. Pooja Raundale  
Director  
Board of Examinations & Evaluation

# University of Mumbai

**1T01824**

Winter 2025

## EXAMINATION TIME TABLE

**PROGRAMME - S.E. Artificial Intelligence & Machine Learning (R-2019-20) ('C' Scheme)****SEMESTER - IV**

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	38921	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	38922	Analysis of Algorithm
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	38923	Database Management System
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	38924	Operating System
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	38925	Microprocessor

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Mumbai - 400 098  
20<sup>th</sup> October, 2025.

*Pooja Raundale*  
Dr. Pooja Raundale  
Director  
Board of Examinations & Evaluation

# University of Mumbai

**1T01434**

Winter 2025

## EXAMINATION TIME TABLE

### PROGRAMME - S.E. (Mechanical) (Choice Based) (R-2020-21 'C' Scheme)

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	41221	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	41222	Fluid Mechanics
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	41223	Kinematics of Machinery
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	41224	CAD/CAM
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	41225	Industrial Electronics

Important Note: •The candidates appearing for the examination should report 20 minutes before the start of examination.

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Mumbai - 400 098  
20<sup>th</sup> October, 2025.

*Y. Raundale*  
Dr. Pooja Raundale  
Director  
Board of Examinations & Evaluation

# University of Mumbai



1T01884

Winter 2025

## EXAMINATION TIME TABLE

**PROGRAMME - S.E. Computer Science & Engineering ( Internet of Things & Cyber Security Including Block Chain Technology) (R-2019-20) ('C' Scheme)**

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	38981	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	38982	Analysis of Algorithm
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	38983	Database Management System
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	38984	Operating System
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	38985	Microprocessor

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Mumbai - 400 098  
 20<sup>th</sup> October, 2025.

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 Dr. Pooja Raundale  
 Director  
 Board of Examinations & Evaluation

# University of Mumbai



1T01234

Winter 2025

## EXAMINATION TIME TABLE

**PROGRAMME - S.E. (Information Technology) (Choice Based) (R-2020-21  
Scheme)**

'C'

### SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	41021	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	41022	Computer Network & Network Design
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	41023	Operating System
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	41024	Automata Theory
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	41025	Computer Organization & Architecture

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Mumbai - 400 098  
 20<sup>th</sup> October, 2025.

*Pooja Raundale*  
 Dr. Pooja Raundale  
 Director  
 Board of Examinations & Evaluation

# University of Mumbai



1TO1034

Winter 2025

## (REVISED) EXAMINATION TIME TABLE

### PROGRAMME - S.E. (Electronics & Telecommunication) (Choice Based) (R-2020-21 'C' Scheme)

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	40821	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	40822	Microcontrollers
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	40823	Linear Integrated Circuits
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	40824	Signals & Systems
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	40825	Principles of Communication Engineering

Important Note: • The candidates appearing for the examination should report 20 minutes before the start of examination.  
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*yraundale*

Mumbai - 400 098  
10<sup>th</sup> November, 2025.

Dr. Pooja Raundale  
Director  
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